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<u>L30</u>	L27 and l9	70	<u>L30</u>
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<u>L13</u>	(718/102-108)[CCLS]	4445	<u>L13</u>
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<u>L9</u>	(712/230-248)![CCLS]	3335	<u>L9</u>
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<u>L7</u>	(712/2-300)[CCLS]	12968	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	12968	<u>L6</u>
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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard



1. A dynamic multithreading processor

Akkary, H.; Driscoll, M.A.;

[Microarchitecture, 1998. MICRO-31. Proceedings. 31st Annual ACM/IEEE International Symposium](#)
30 Nov.-2 Dec. 1998 Page(s):226 - 236

Digital Object Identifier 10.1109/MICRO.1998.742784

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IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
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 Partovi, H.; Weber, F.; White, S.; Wood, T.; Yong, J.;
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 Volume 34, Issue 11, Nov. 1999 Page(s):1466 - 1477
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[High-Performance Computer Architecture, 2001. HPCA. The Seventh International Symposium on](#)
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 Digital Object Identifier 10.1109/HPCA.2001.903250
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 Hesley, S.; Andrade, V.; Burd, B.; Constant, G.; Correll, J.; Crowley, M.; Golden, M.; Hopkins, N.; I.
 Khondker, R.; Meyer, D.; Moench, J.; Partovi, H.; Posey, R.; Weber, F.; Yong, J.;
[Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC, 1999 IEEE International](#)
 15-17 Feb. 1999 Page(s):92 - 93
 Digital Object Identifier 10.1109/ISSCC.1999.759134
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- 5. **A Case for Fault Tolerance and Performance Enhancement Using Chip Multi-Processors**
 Zhou Huiyang ;
[IEEE Computer Architecture Letters : Accepted for future publication](#)
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